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Park, J.; Moon-Hee Choi; Won-Kee Hong; Woo-Chan Park; Shin-Dug Kim;
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








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<u>10777294</u>	Not Issued	030	02/12/2004	IMAGE PROCESSING METHOD, IMAGE PROCESSING APPARATUS, STORAGE MEDIUM AND PROGRAM	ENOMOTO, HIROMICHI
<u>10187709</u>	Not Issued	030	07/05/2002	COLLECTION AND ACCUMULATION SYSTEM FOR PACKETS WITH TIME INFORMATION	ENOMOTO, HIROMICHI
<u>10059328</u>	<u>6643189</u>	150	01/31/2002	MEMORY DEVICE	ENOMOTO, HIROMICHI
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<u>09861970</u>	<u>6816620</u>	150	05/21/2001	METHOD OF PRESERVING PHOTOGRAPHED IMAGES IN ACCORDANCE WITH THE DATE OF PHOTOGRAPHING	ENOMOTO, HIROMICHI
<u>09803525</u>	Not Issued	041	03/09/2001	IMAGE SEARCHING AND VIEWING SYSTEM	ENOMOTO, HIROMICHI
<u>09750040</u>	<u>6359812</u>	150	12/29/2000	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>09428925</u>	<u>6198665</u>	150	10/28/1999	ONE CHIP SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING TWO MODES OF DATA WRITE OPERATION AND BITS SETTING OPERATION	ENOMOTO, HIROMICHI
<u>09303442</u>	<u>6028795</u>	150	05/03/1999	ONE CHIP SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING TWO MODES OF DATA WRITE OPERATION AND BITS SETTING OPERATION	ENOMOTO, HIROMICHI

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<u>08853713</u>	<u>5781479</u>	150	05/09/1997	MEMORY DEVICE	ENOMOTO, HIROMICHI
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<u>08408283</u>	<u>5477486</u>	150	03/22/1995	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>08354934</u>	<u>5548744</u>	150	12/12/1994	MEMORY CIRCUIT AND METHOD FOR SETTING AN OPERATION MODE	ENOMOTO, HIROMICHI
<u>08294407</u>	<u>5448519</u>	150	08/23/1994	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>08294406</u>	<u>5838337</u>	150	08/23/1994	GRAPHIC SYSTEM INCLUDING A PLURALITY OF ONE CHIP SEMICONDUCTOR INTEGRATED CIRCUIT DEVICES FOR DISPLAYING PIXEL DATA ON A GRAPHIC DISPLAY	ENOMOTO, HIROMICHI
<u>08294405</u>	<u>5767864</u>	150	08/23/1994	ONE CHIP SIMICONDOCTOR INTEGRATED CIRCUIT DEVICE FOR DISPLAYING PIXEL DATA ON A GRAPHIC DISPLAY	ENOMOTO, HIROMICHI
<u>08294404</u>	<u>5475636</u>	150	08/23/1994	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>08294403</u>	<u>5424981</u>	150	08/23/1994	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>08123357</u>	Not Issued	166	09/17/1993	MEMORY DEVICE WITH LOGIC FUNCTION FOR	ENOMOTO, HIROMICHI

				SETTING OPERATION MODE THEREOF	
<u>08013174</u>	<u>5265234</u>	150	01/29/1993	INTEGRATED MEMORY CIRCUIT AND FUNCTION UNIT WITH SELECTIVE STORAGE OF LOGIC FUNCTIONS	ENOMOTO, HIROMICHI
<u>07935919</u>	<u>5434863</u>	150	08/27/1992	INTERNETWORKING APPARATUS FOR CONNECTING PLURAL NETWORK SYSTEMS AND COMMUNICATION NETWORK SYSTEM COMPOSED OF PLURAL NETWORK SYSTEMS MUTUALLY CONNECTED	ENOMOTO, HIROMICHI
<u>07868719</u>	<u>5354610</u>	150	04/14/1992	MAGNETIC RECORDING MEDIUM COMPRISING A CONDUCTIVE UNDERLAYER CONTAINING CARBON BLACK AND A MAGNETIC LAYER CONTAINING TABULAR BARIUM FERRITE MAGNETIC PARTICLES, ALPHA ALUMINA, AND BINDER RESIN	ENOMOTO, HIROMICHI
<u>07855843</u>	<u>5450342</u>	150	03/20/1992	MEMORY DEVICE	ENOMOTO, HIROMICHI
<u>07816583</u>	Not Issued	166	01/03/1992	INTEGRATED CIRCUIT AND FUNCTION UNIT WITH SELECTIVE STORAGE OF LOGIC FUNCTIONS	ENOMOTO, HIROMICHI
<u>07711254</u>	<u>5148539</u>	150	06/04/1991	ADDRESS BUS CONTROL APPARATUS	ENOMOTO, HIROMICHI
<u>07542028</u>	<u>RE33922</u>	150	06/21/1990	MEMORY CIRCUIT FOR GRAPHIC IMAGES	ENOMOTO, HIROMICHI
<u>07497726</u>	<u>5123093</u>	150	03/19/1990	AN OPERATIONAL PROCESSOR FOR PERFORMING A MEMORY ACCESS AND AN OPERATIONAL PROCESS IN PARALLEL	ENOMOTO, HIROMICHI
<u>07314238</u>	<u>5113487</u>	150	02/22/1989	MEMORY CIRCUIT WITH LOGIC FUNCTIONS	ENOMOTO, HIROMICHI
<u>07256402</u>	Not Issued	166	10/11/1988	ADDRESS BUS CONTROL APPARATUS	ENOMOTO, HIROMICHI

07231000	4990907	150	08/11/1988	METHOD AND APPARATUS FOR DATA TRANSFER	ENOMOTO, HIROMICHI
07140790	Not Issued	166	01/05/1988	PARALLEL MIMORY ACCESS OPERATIONAL PROCESSOR	ENOMOTO, HIROMICHI
07014454	4751124	150	02/13/1987	INFORMATION RECORDING MEDIUM	ENOMOTO, HIROMICHI
06865376	4757473	150	05/21/1986	MEMORY CIRCUIT	ENOMOTO, HIROMICHI
06864502	Not Issued	166	05/19/1986	MEMORY CIRCUIT WITH LOGIC FUNCTIONS	ENOMOTO, HIROMICHI

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